

Software-in-the-Loop using virtual CAN buses otiation

Motivation

5 Series Transmission Software development

7 Simulation software development

7 Hardware software from 9 : ; supplier

7 Different software variants

7 Code ' ', and .oded,
auto .ode from TargetLink(

5 Frontload development tasks

7 Debugging of series transmission



Software-in-the-Loop using virtual CAN buses otivation

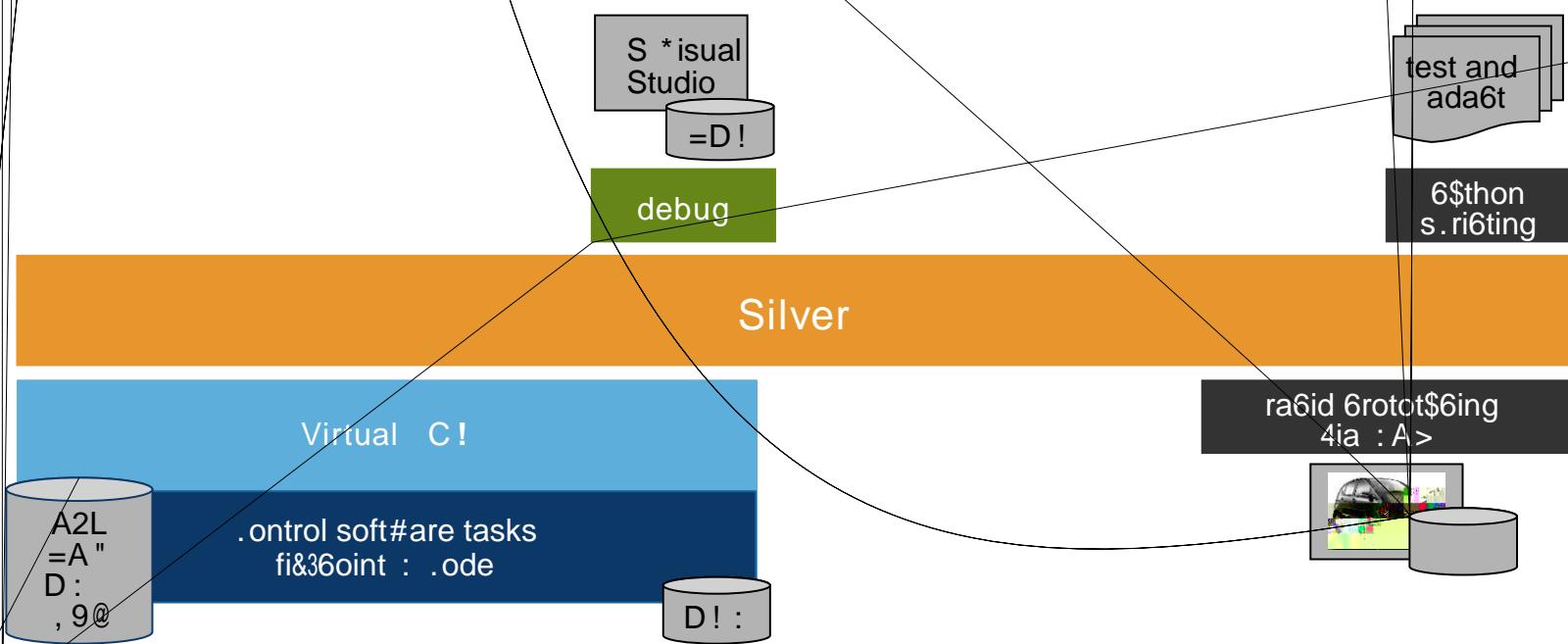
5 Software#are#in#the#Loop 'SiL(

7)ntegration Signals@0e5000L .6r



Software-in-the-Loop using virtual CAN buses Sil4er

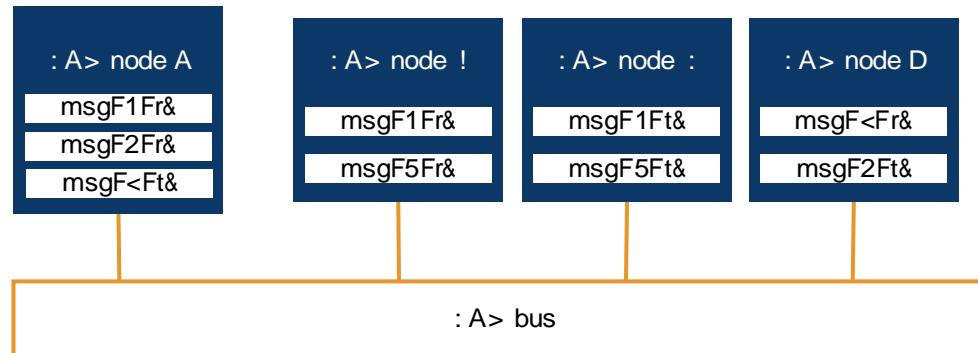
- 5 Sil4er from - Troni. +mb ,
7 Soft#are3in3the3Loo6 'SiL(simulation en4ironment
7 All rele4ant automoti4e standard formats su66orted 'A2L, =A ", D ! : (
- 5)A * has already been used with Sil4er for 2 years



Software-in-the-Loop using virtual CAN buses

: A> !asi.s

- 5 !us! Bne transmitter 3 multi6le re.ei4ers
- 5 >ode! t\$6i.all\$ one 9 : ; , transmits or re.ei4es messages
- 5 message! u63to E !\$te data, .\$.li. or e4ent3based, 6riorit\$
- 5 Signal! 6a.ked into a message, s.aled b\$ gain1offset '13C? bit(
- 5 D! : 8ile! s6e.ifies : A> bus, es6e.iall\$ message stru.ture and timing



Software-in-the-Loop using virtual CAN buses

: A> !asi.s0 rest bus

5 SiL simulation fo.used on one 9 : ;

7 D! : defines #hole bus

7 >ot all nodes1 messages are needed in simulation

7 9mulation filters b\$

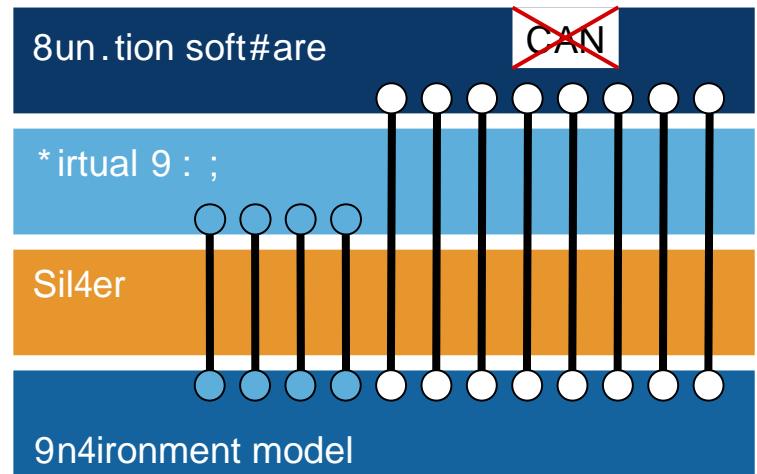
5 node names

5 bla.k listing or #hite listing of messages

" est bus : A> '6lant(

Software-in-the-Loop using virtual CAN buses Signals in .ommon SiL

```
5 :ommon SiL signal rooting '#ithout :A>(
7 *irtual 9 : ;18un.tion soft#are 'Sil4er : A=)(  
    5 "emo4e :A> .ode  
    5 define Silver &'O for .ode 4ariable  
        'gain and offset manuall$)  
7 odel 'Sil4er Simulink blo.k set(  
    5 define Silver &'O for Simulink signal  
  
5 Sil4er  
7 dete.ts Sil4er )1B signals b$ name  
7 .o6ies information automati.all$ at  
begin1 end of simulation ste6  
7 .onne.tion data t$6el double  
  
5 Summar$  
7 8un.tion soft#are :A> .ode is b$6assed  
7 T$6i.all$ man$ signals to be set u6
```



Legend
● =in Sil4er)1B
○ : A> Sil4er)1B

Software-in-the-Loop using virtual CAN buses Signals in SiL #ith :A> '2(

5 Summary

7 Function software :A> .ode in simulation,
.an be debugged and tested

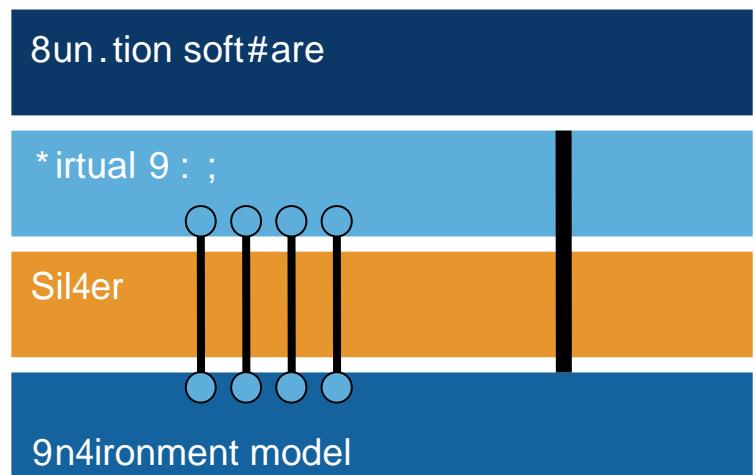
7 "edu.ed .ommuni.ation setu6 effort

5 Less manual definitions

5 Automati. s.aling from1 to :A>
message

7 ;se of D ! : #ork 6rodu.t

7 :ar .om6arable .ommuni.ation beha4iour



Software-in-the-Loop using virtual CAN buses using Sil4er : A> emulation : ode

5 Sil4er A=) 2.% defines : fun.tions for

7 :onfigure :A> bus or busses b\$ D ! : file or b\$ single messages

7 Start1 sto6 :A> emulation

7 :he.k ne# message re.ei4ed

7 Transmit1 re.ei4e message 'E b\$te data(

7 Transmit1 re.ei4e signal1 4ariable 'from a message, ra# or s.aled(

7 ani6ulate messages for .ounter1 : " : '.all3ba.k dll(

* irtual 9 : ;

The screenshot shows a software interface for configuring a virtual CAN bus. The configuration parameters listed are:

- bus_id: 0 // bus identifier
- bus_name: "virtual" // bus name
- can_id: 1 // can bus ID
- file_name: "trans_dfb" // file name
- ignore_file: "ignore.txt" // ignore file name
- loopback: 0 // loopback mode
- max_tx_size: 8 // max tx size
- rx_timeout: 1000 // rx timeout

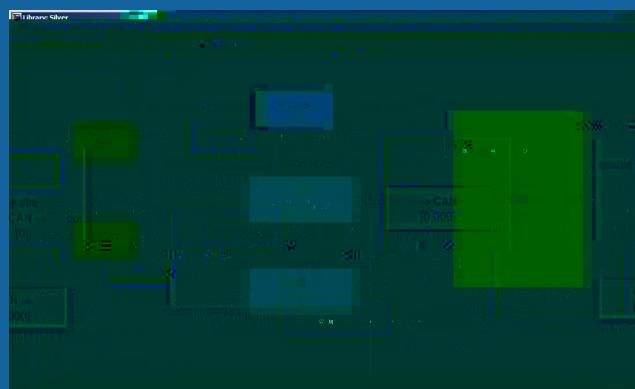
Software-in-the-Loop using virtual CAN buses ; sing Sil4er : A> emulation Simulink

5 Sil4er : A> blo.k set

7 ! us message setu6 'Sil4er : A>msg ! us(
: onfigure one : A> bus b\$ D ! : and node names
9nable1 disable node b\$ Simulink in6ut bus
Transmit messages from Simulink in6ut bus
But6ut re.ei4ed messages to Simulink out6ut bus

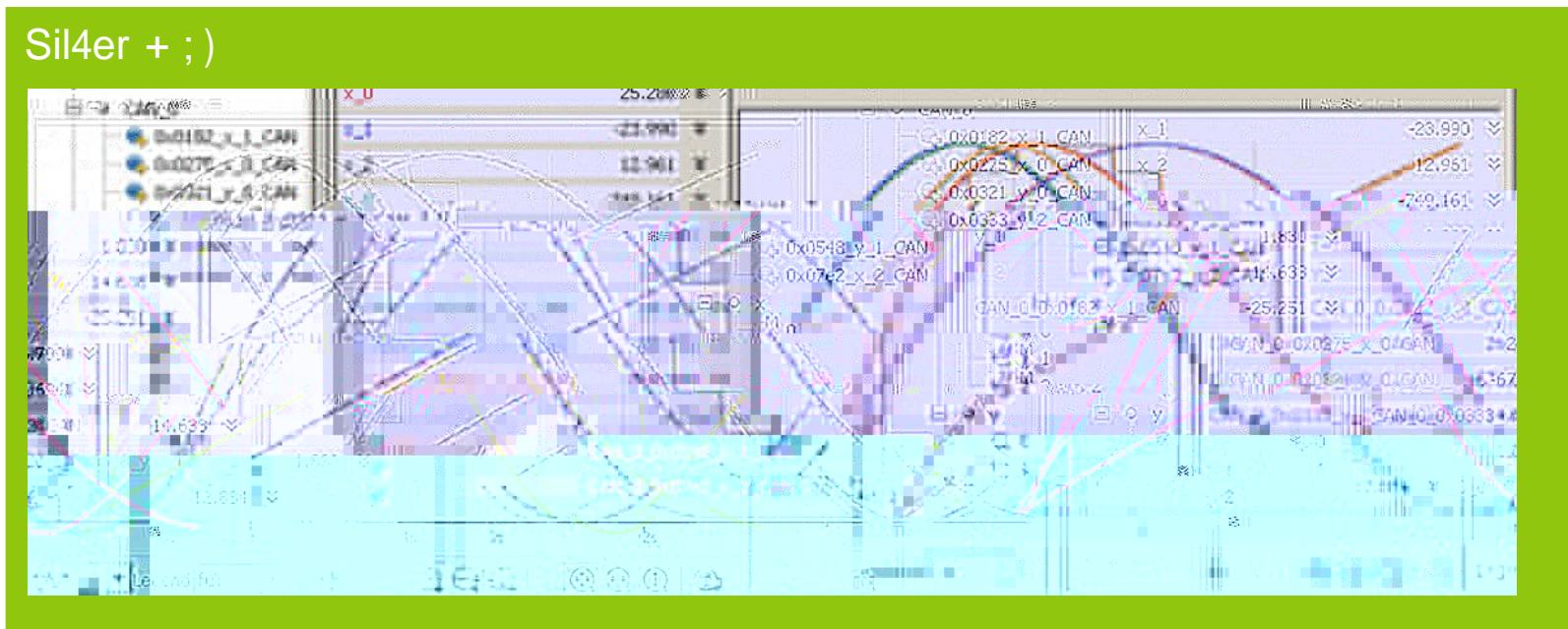
7 Single message setu6 'Sil4er : A>bus : onfig, Sil4er : A>bus " ead,
Sil4er : A>bus l rite(

9n4ironment model



Software-in-the-Loop using virtual CAN buses ; sing Sil4er :A> emulation) ; ser + ;)

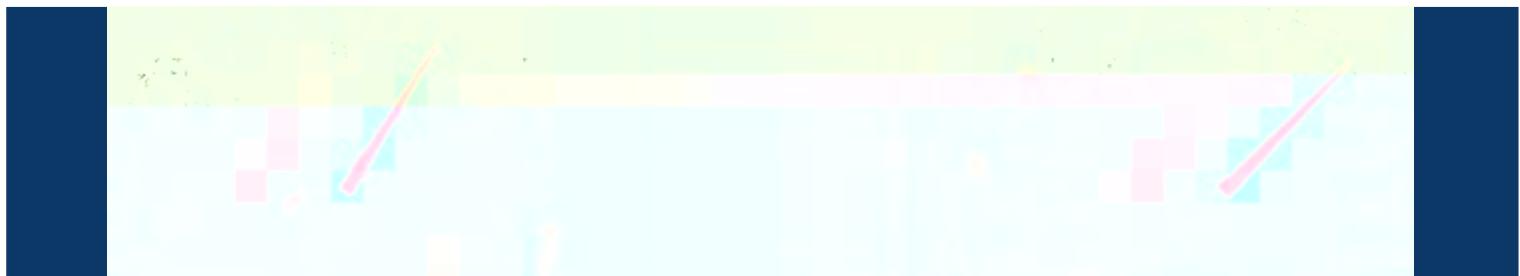
- 5 See .onnection state of :A> messages
 - 5 9as\$ address to :A> signals for slotting\ debugging



Software-in-the-Loop using virtual CAN buses

Summar\$

- 5 >et#ork .ommuni.ation is an im6ortant 6art of s\$stem design
- 5 ; sing :A> emulation in SiL simulations
 - 7 drasti.all\$ redu.es the effort for defining SiL .ommuni.ation setu6
 - 7 im6ro4ing the .onsisten.\$ of definitions
 - 7 adds additional simulation as6e.ts 'signal s.ale, .ommuni.ation timing(
 - 7 enables tests of the :A>3related soft#are 6arts '#hi.h are u6 to no# b\$6assed(



Thank \$ouJ

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