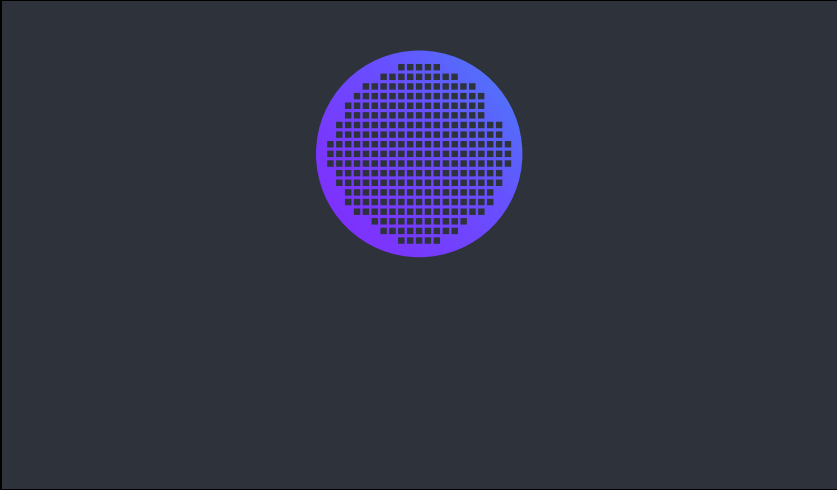
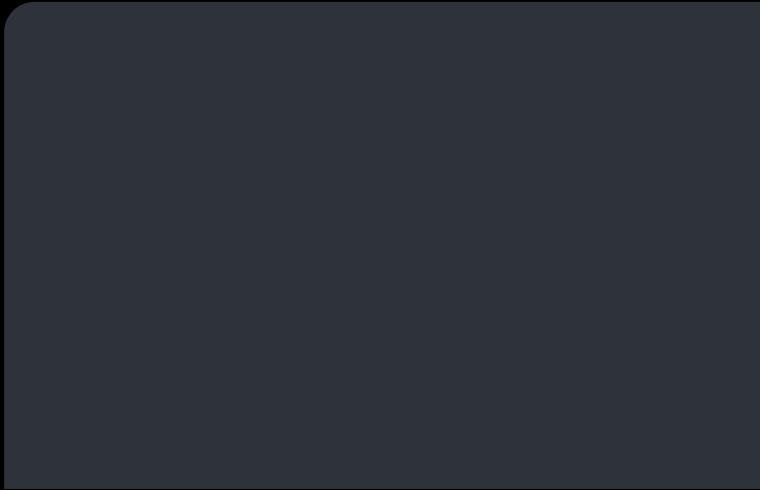
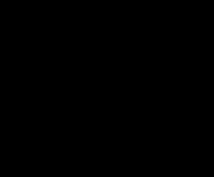


Sassine Ghazi

Cautionary Statement Regarding Forward-Looking Statements

This presentation contains forward-looking statements, including, but not limited to, statements regarding strategies related to our products, technology and services; business and market outlook, opportunities, strategies and technological trends, such as artificial intelligence; customer demand and market expansion; our planned product releases and capabilities; and industry growth rates. These statements involve risks, uncertainties and other factors that could cause our actual results, time frames or achievements to differ materially from those expressed or implied in such forward-looking statements. Such risks, uncertainties and factors include, but are not limited to: macroeconomic conditions and geopolitical uncertainty in the global economy; uncertainty in the growth of the semiconductor and electronics industries; the highly competitive industry we operate in; actions by the U.S. or foreign governments, such as the imposition of additional export restrictions or tariffs;





Reinvention of
computing

Explosion of
intelligent systems



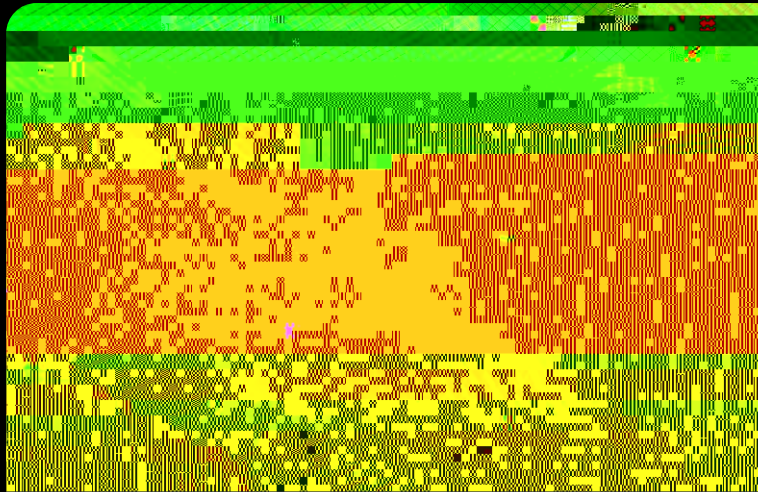
Unprecedented transformation

— — — — —

|



Challenges



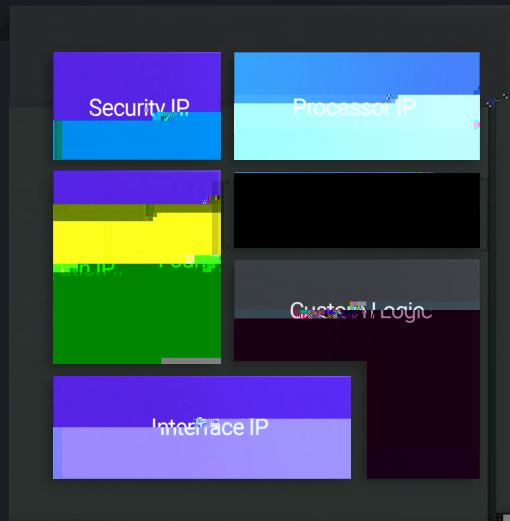
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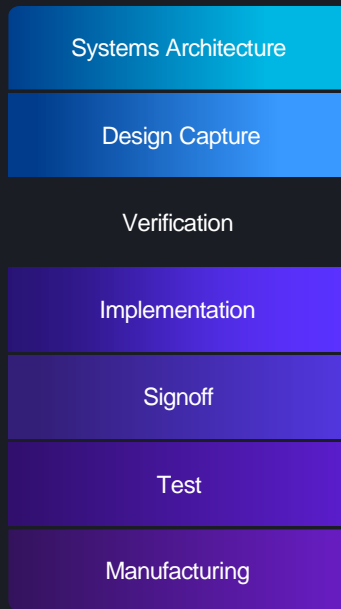
TESLA

Silicon design solutions

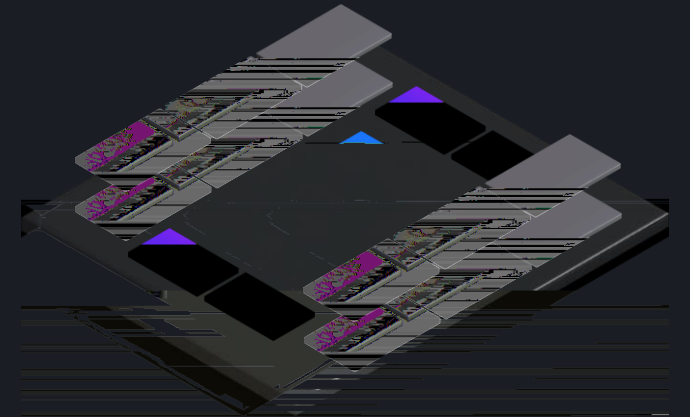
Most Trusted and
Broadest IP Portfolio



Most Comprehensive
EDA Platform

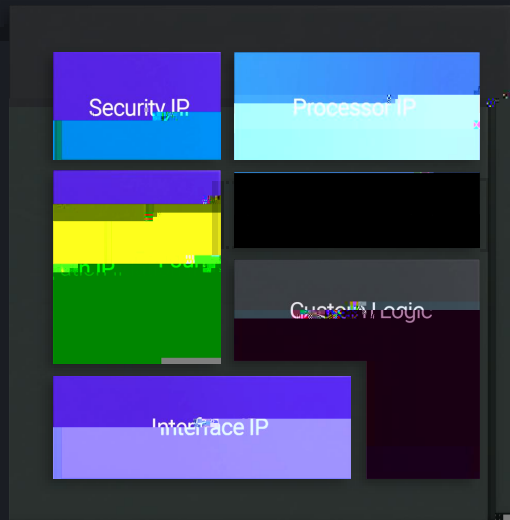


Most Advanced
Multi-Die Solutions

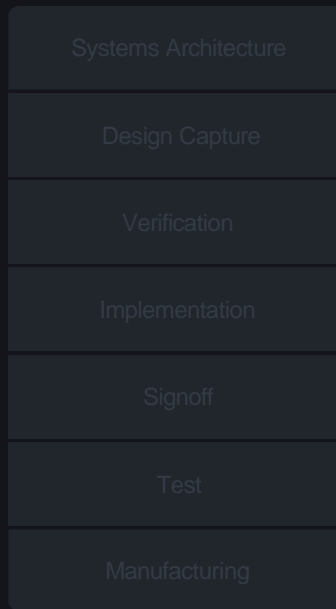


Silicon design solutions

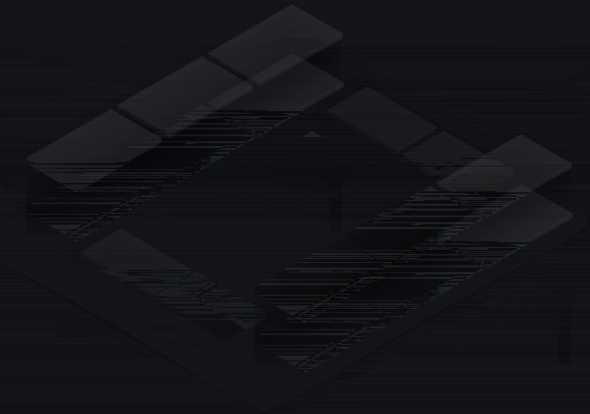
Most Trusted and
Broadest IP Portfolio



Most Comprehensive
EDA Platform



Most Advanced
Multi-Die Solutions



Silicon design solutions

Most Comprehensive
EDA Platform



Ext.

Circuit
Design

Char.

P&R

STA

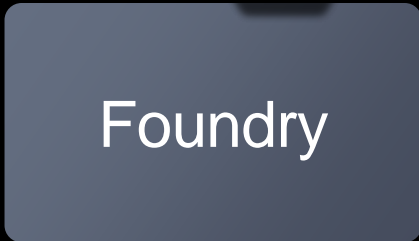
Phys.
Verif.



Ext.



Foundry



Foundry

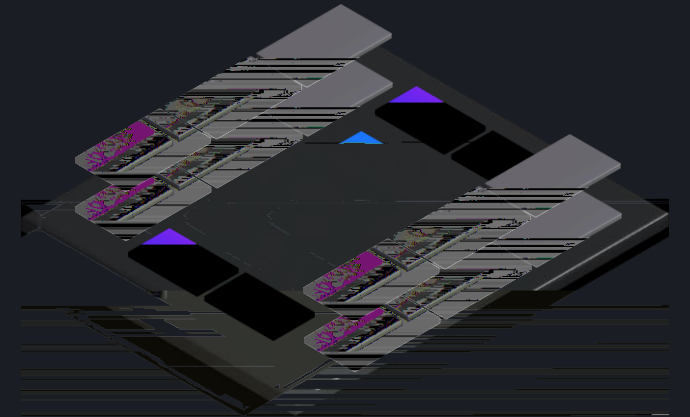


Designer



Silicon design solutions

Most Advanced
Multi-Die Solutions







Announcing

Synopsys Platform Architect™ Multi-Die

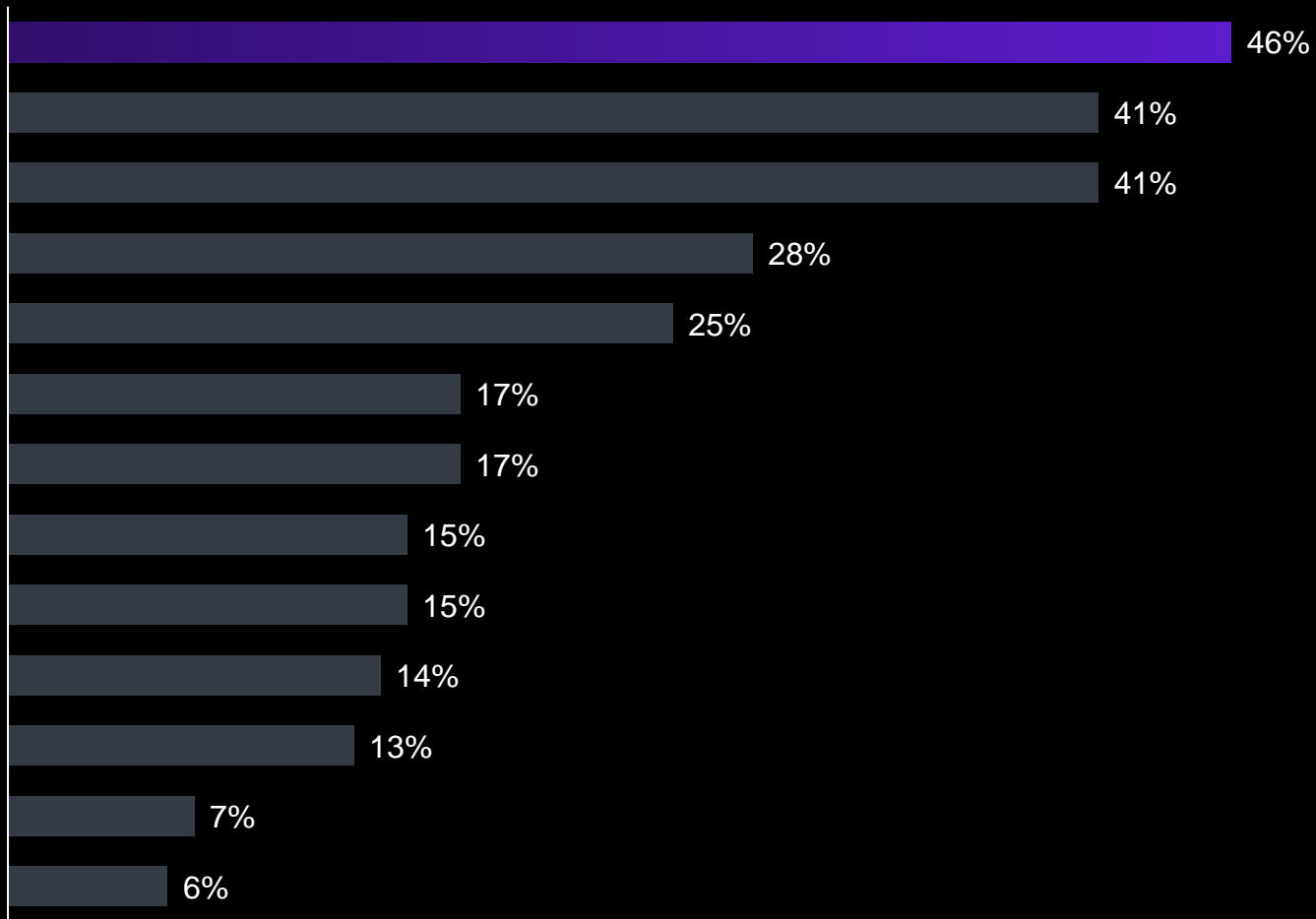
Announcing

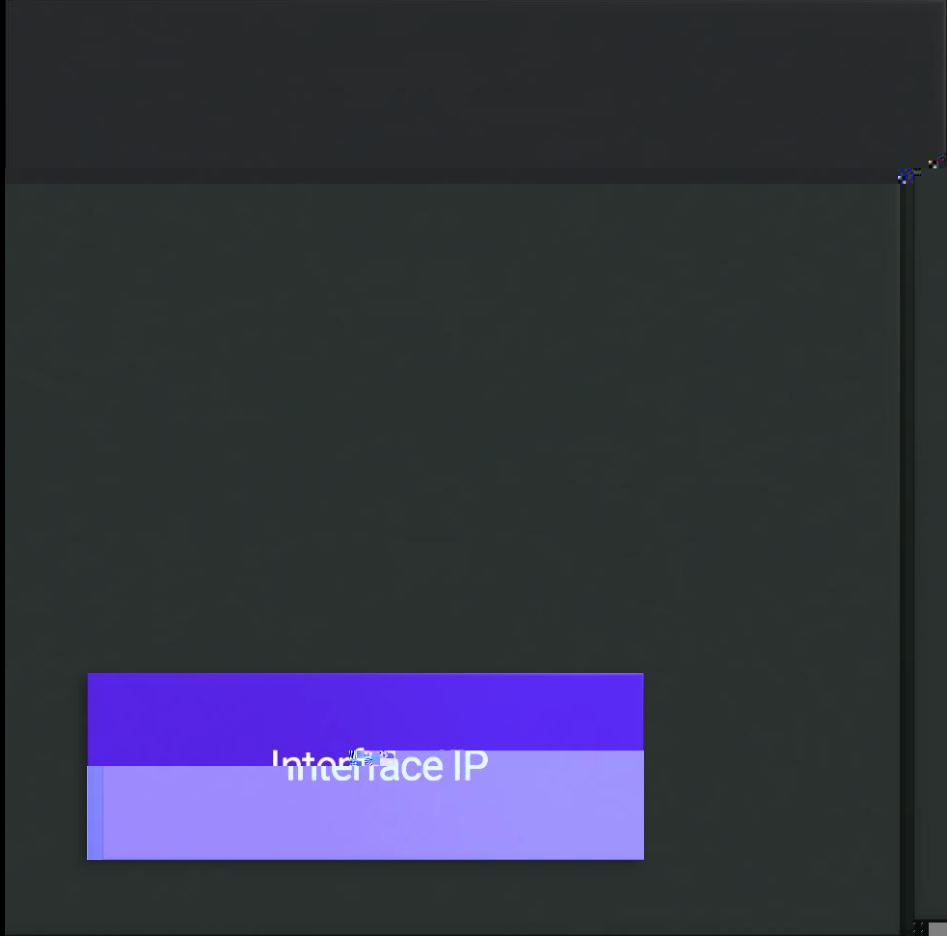
Synopsys 3DSO.ai™

Synopsys silicon ecosystem

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Interface IP

Boost productivity with world -class IP,
accelerate time to market



Systems Architecture

Design Capture

AI-powered

Systems Architecture

Design Capture

Verification

Implementation

Signoff

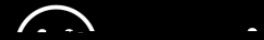
Test

Manufacturing

Up to 40%

Announcing

Synopsys Cloud Hybrid Solution





Systems Companies

Customized silicon essential for differentiation



Software-Defined Products



Silicon Companies

Software workloads driving silicon requirements

Software-driven
architecture exploration

Software-driven
design verification

SYNOPSYS[®]

Industry-leading system design solutions

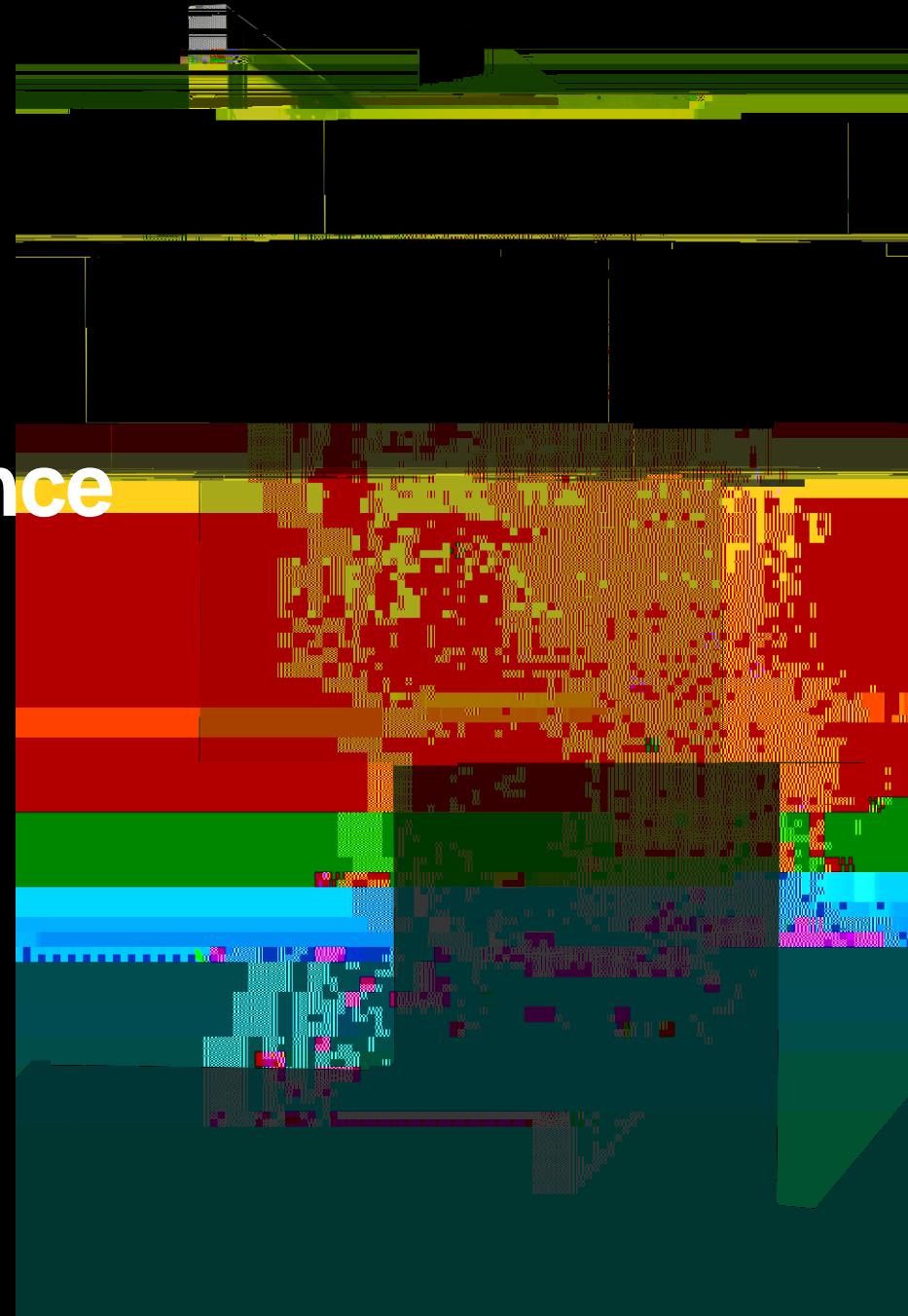
Software and
system validation

Silicon lifecycle
management

Announcing

Synopsys HAPS[®]-100 12

**HAPS leading prototyping performance
for up to**

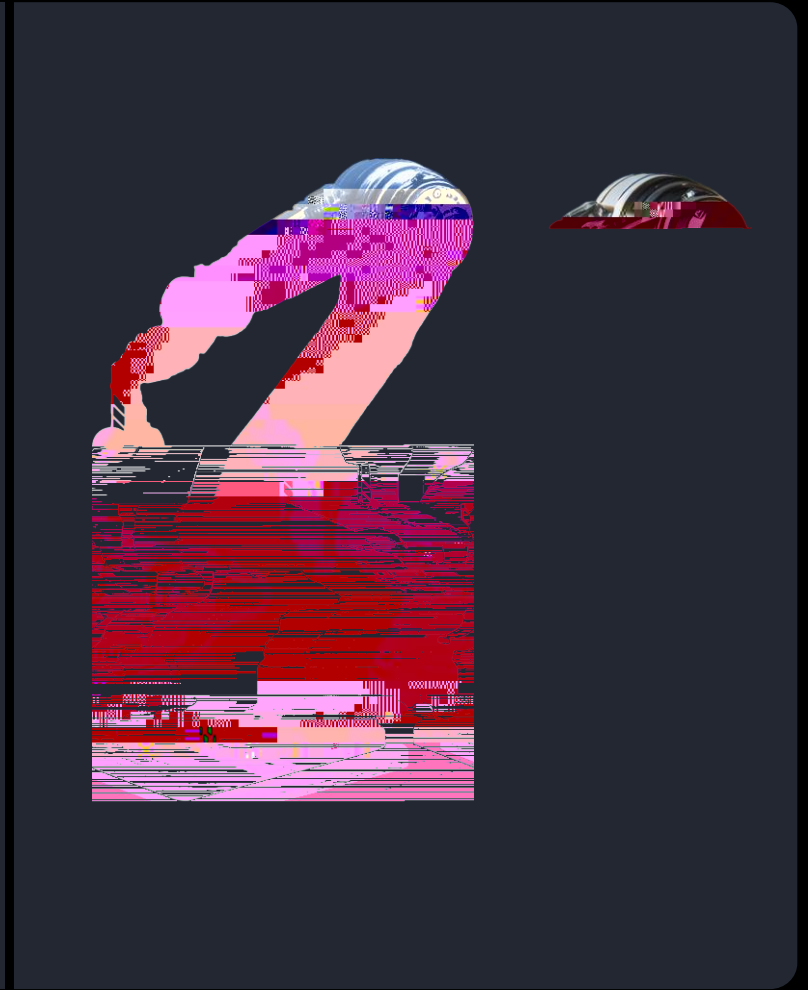
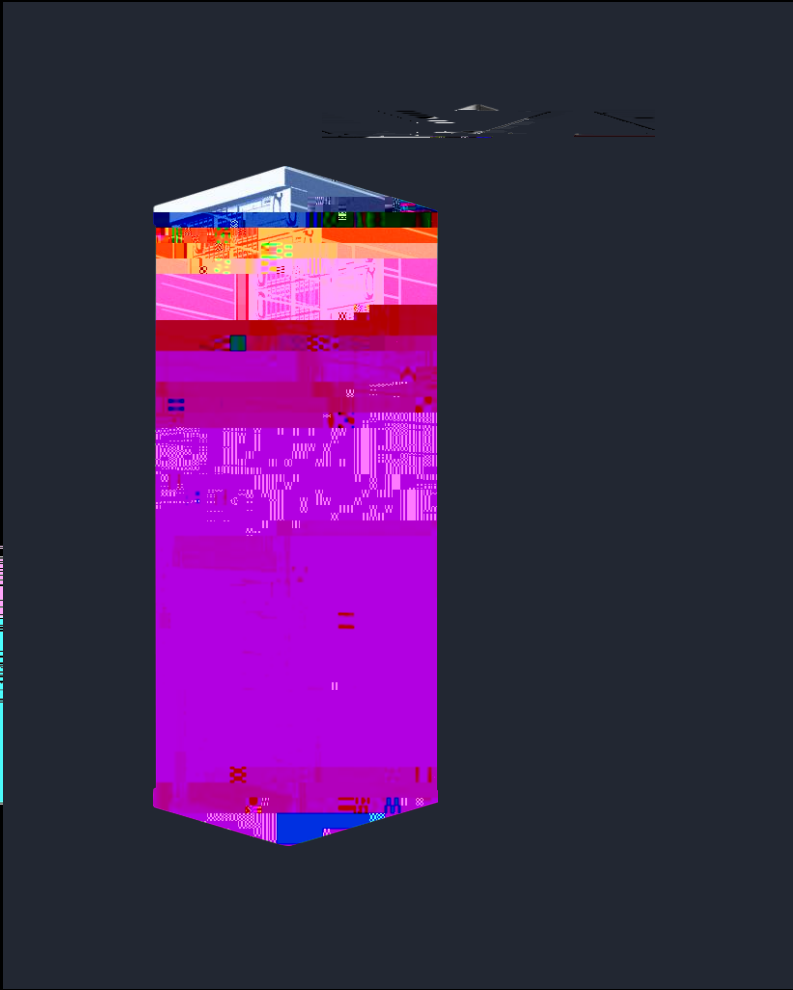
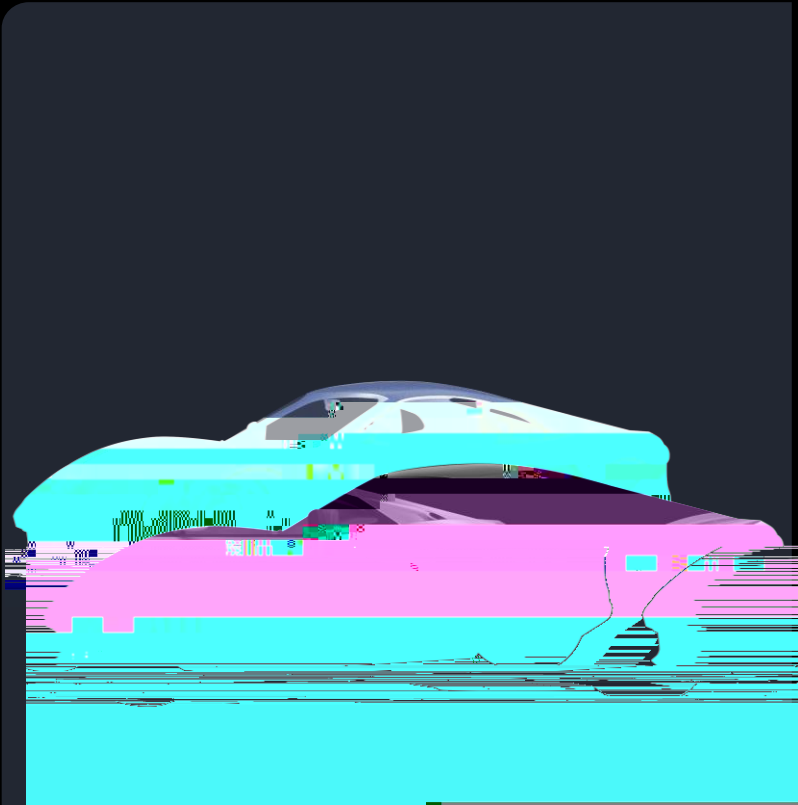


Announcing

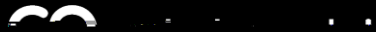
Synopsys ZeBu[®] EP2

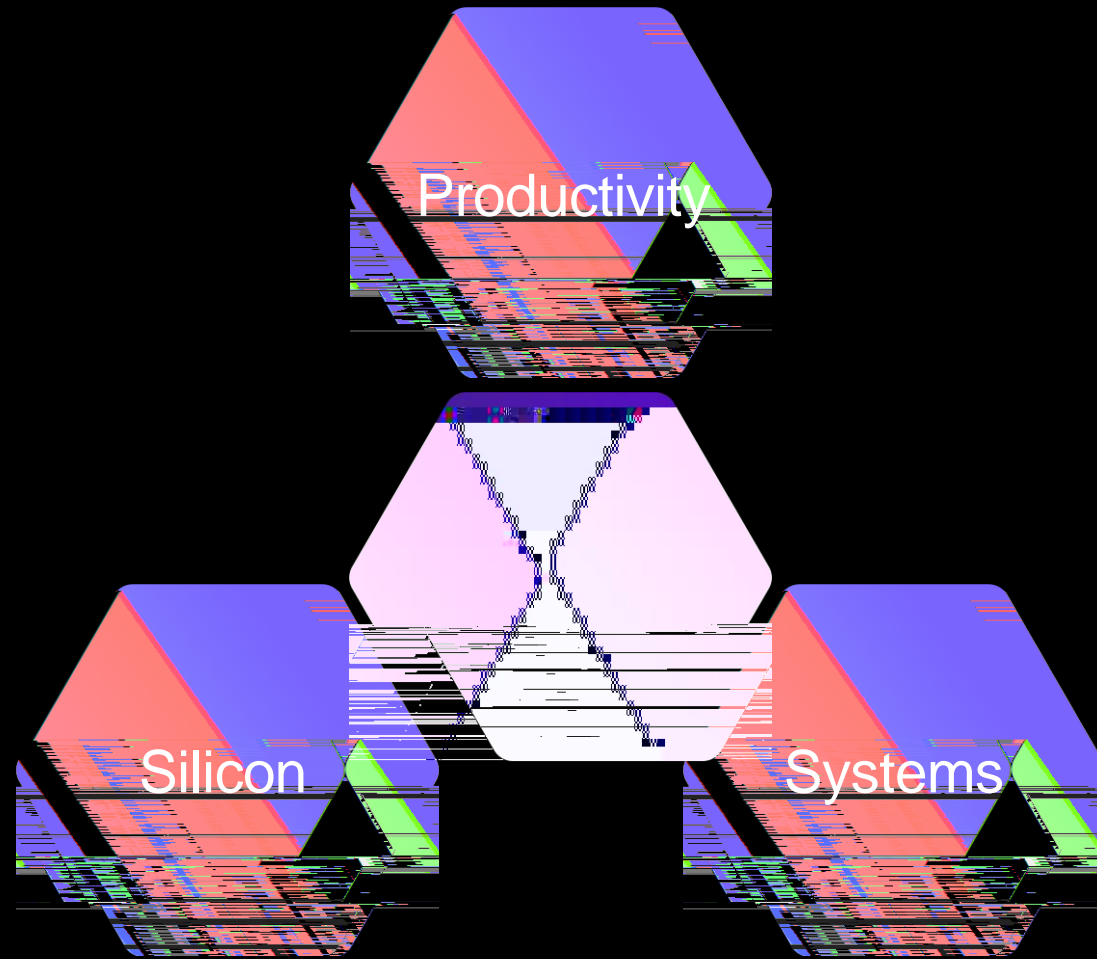
ighest performance for all hardware

Electronics digital twin



Synopsys systems ecosystem





Synopsys Ecosystem



TESLA

