应用于边缘人工智能，基于USB4的灵活接口IP解决方案

作者

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USB/DisplayPort

AI

SoC

SoC USB4

IP IP PCIe 4.0 USB4 USB 3.x USB 2.0

介绍

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SoC

SoC

USB 3.x USB 3.x

SoC

SoC PCIe

PCIe USB4 PCIe
CPU USB4
USB4

PCIe USB4
USB USB 3.x
USB4 USB4

PCIe USB PHY

SoC USB4
PCIe 4.0 USB4 USB 3.x USB 2.0

边缘人工智能计算器举例

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USB4 PCIe S ɣ

USB4带宽

USB4

USB4 USB4 USB4
32Gbps PCIe USB4 USB4 USB4 32Gbps PCIe
USB4 USB4 PCIe USB4

USB USB4 USB4 USB4 USB4
USB4 USB4 PCIe USB4
40Gbps USB4 PCIe

USB USB4 USB4 4K
USB4 DisplayPort PCIe USB4
USB4

传统USB模式

USB USB USB
1 4 USB4/PCIe PHY USB 3.x USBP3 ŋ SBP6 9 ∃ Γ ,

USB4 USB3.x USB
USB AI
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传统USB 2.0模式

USB 2.0 USB3.x xDCI USB4 PHY 2
USB 2.0 PHY USB2.0
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新思科技和USB-IF

USB USB USB 1.1
USB4 USB DesignWare USB IP

USB4资源：

USB4 : https://www.synopsys.com/dw/doc.php/wp/usb4_user_expectations_drive_design_complexity.pdf

USB4 : https://www.usb.org/document-library/usb4tm-specification

USB4 : https://www.usb.org/documents?search=usb4&items_per_page=50